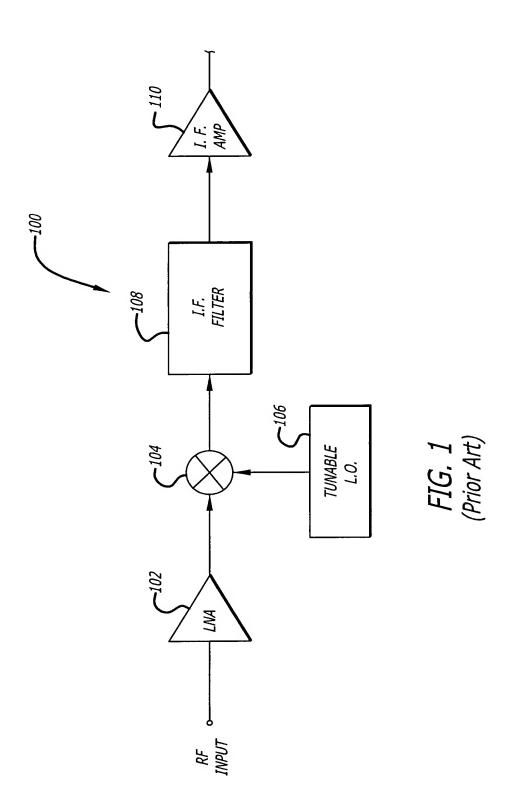
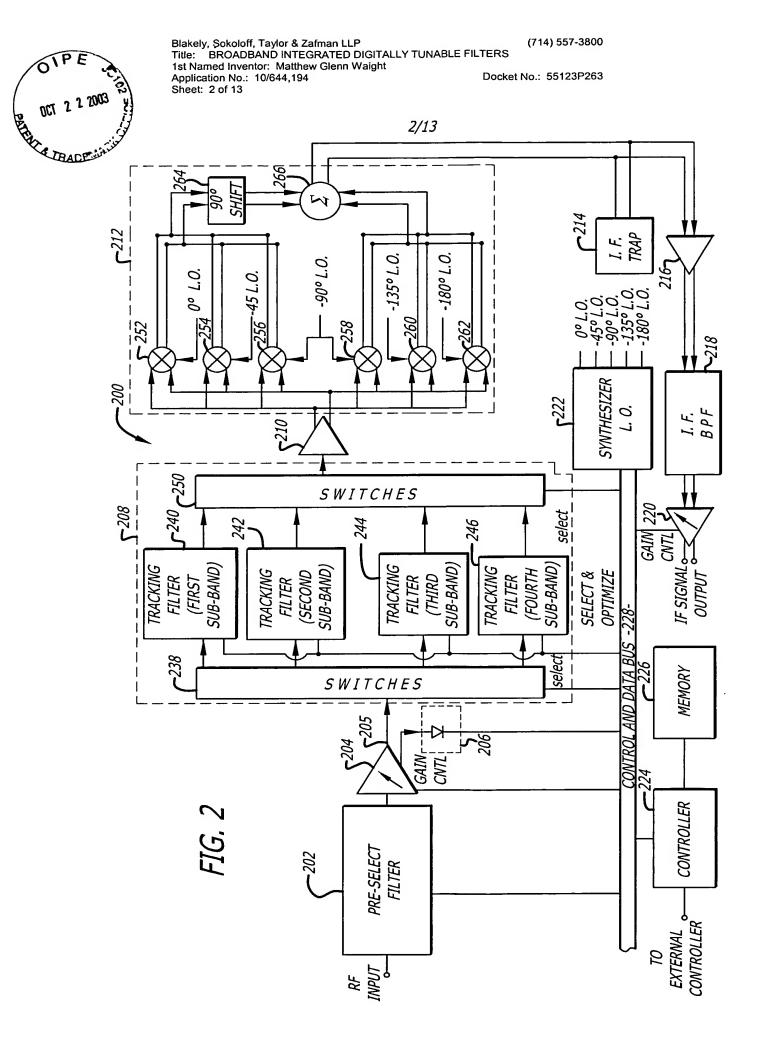
Blakely, Sokoloff, Taylor & Zafman LLP
Title: BROADBAND INTEGRATED DIGITALLY TUNABLE FILTERS
1st Named Inventor: Matthew Glenn Waight
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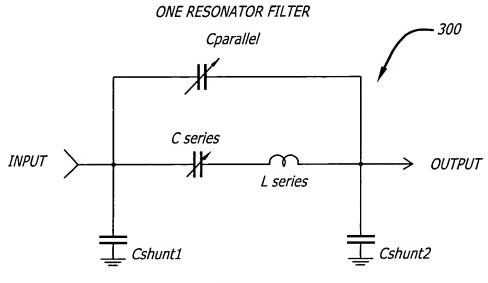
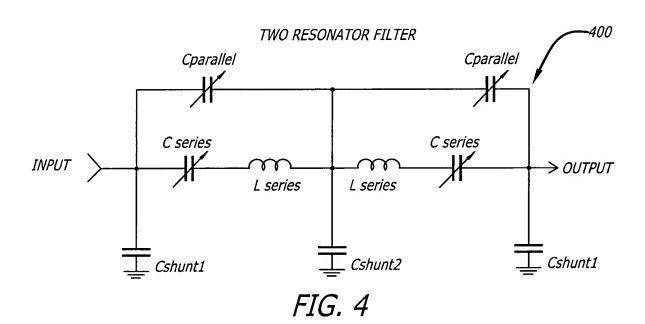


FIG. 3



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Docket
Sheet: 4 of 13 (714) 557-3800 Docket No.: 55123P263 4/13 V 32 C 32 C × Ø -500 Ø  $\mathcal{Z}$  $\mathcal{SC}$ X Ø 40 × × ×

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Title: BROADBAND INTEGRATED DIGITALLY TUNABLE FILTERS

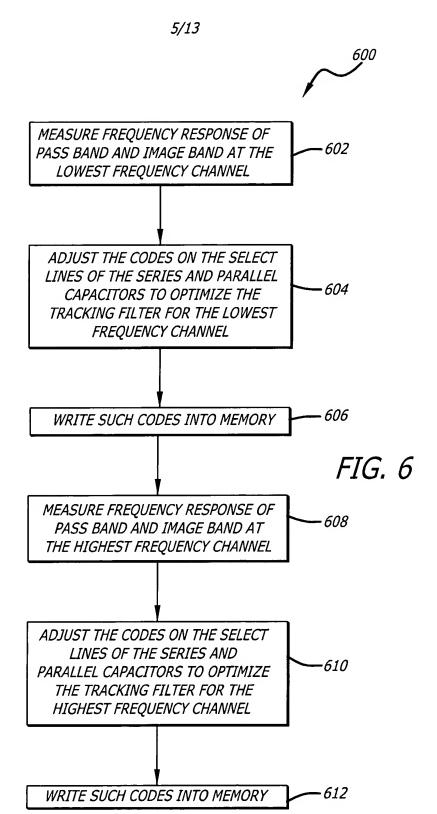
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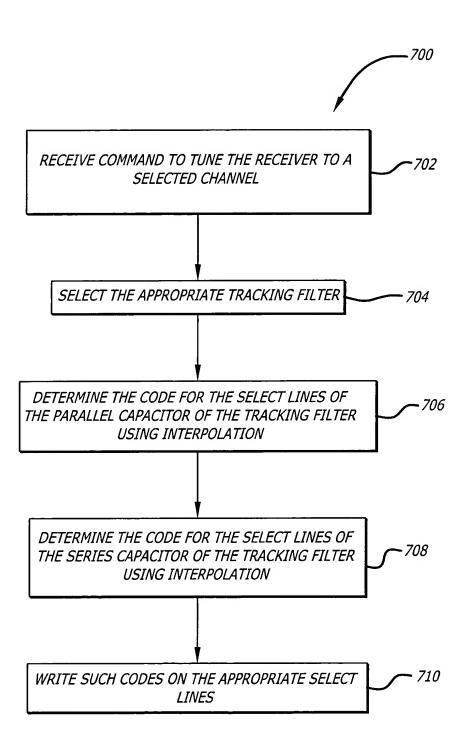


FIG. 7

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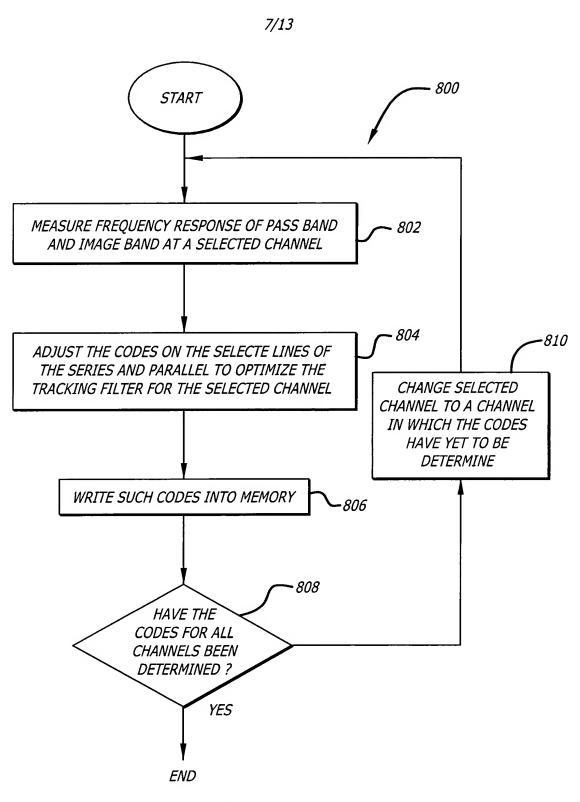


FIG. 8

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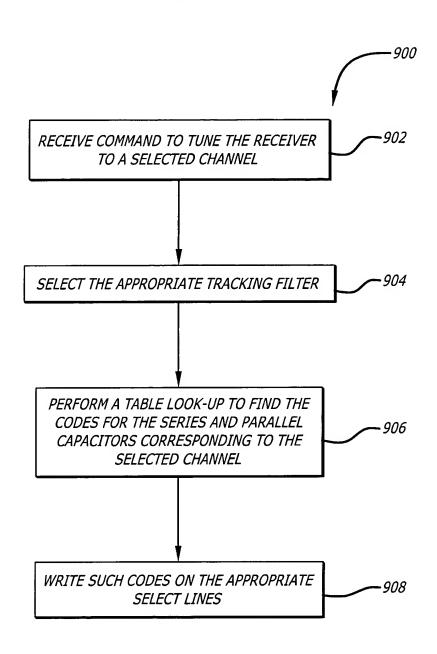


FIG. 9

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F (MHz)	СР	CS
570	27	63
580	26	59
590	25	54
600	24	50
610	23	46
620	23	42
630	22	39
640	21	35
650	20	32
660	19	29
670	18	26
680	17	23
690	16	20
700	15	18
710	14	16
720	14	13
730	13	11
740	12	10
750	11	8
760	10	6
770	9	5
780	8	4 3 2 1
790	7	3
800	6	2
810	5	
820	4	1

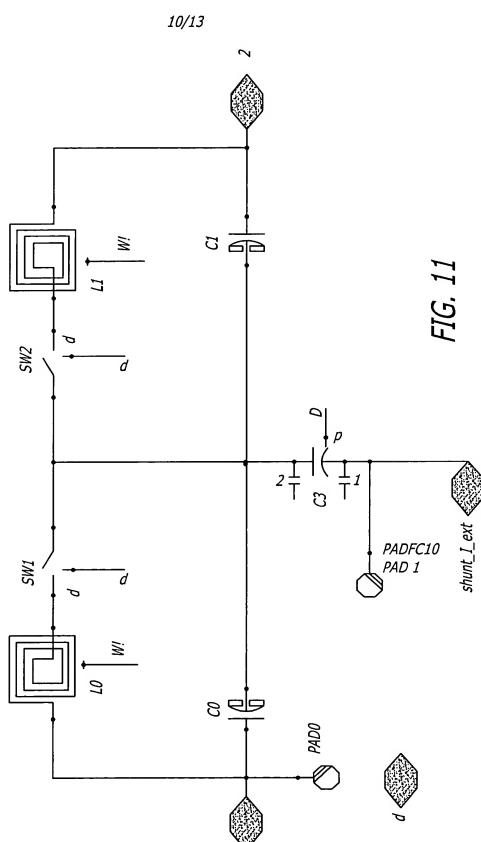
FIG. 10

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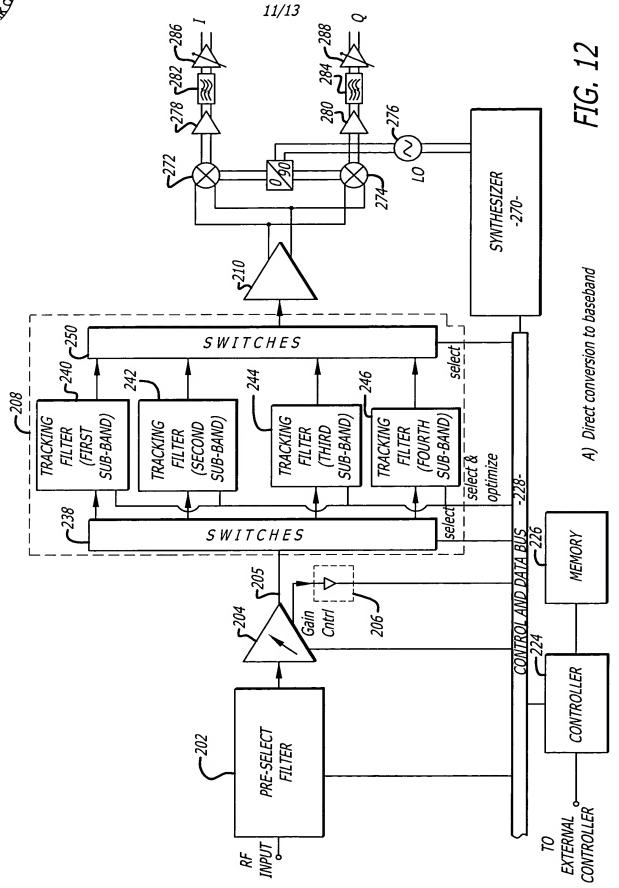


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SUB-BAND)

TRACKING

FILTER

(FIRST



Docket No.: 55123P263 12/13 Interfac Interfac .290 S/D 1 Bit *S/D 1 Bit Aodulator* AGC 

DRIVER 284 282 SYNTHESIZER B) Direct conversion with digital outputs SWITCHES select 242 244 246 TRACKING FILTER (SECOND SÚB-BAND) SUB-BAND) **TRACKING** SUB-BAND) TRACKING (FOURTH (THIRD FILTER select & optimize MEMORY select SWITCHES Gain Cntrl 206 CONTROLLER PRE-SELECT FILTER EXTERNAL CONTROLLER

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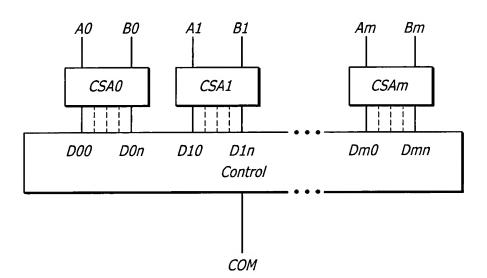


FIG. 14